## Amendments to the Claims

- 1. (Currently Amended) A semiconductor memory which is capable of <u>for</u> executing at least two application programs, comprising:
  - a first storage unit operable to store data relating to a first application program;
  - a second storage unit operable to store data relating to a second application program;
- a level identification unit operable to identify respective security levels of the first and second application programs for the data relating to the first and second application programs, based on a criterion for identifying a security level of an application program for data relating to the application program; and

a duplication unit operable to duplicate the data stored in the first storage unit and to store the duplicated data into the second storage unit, without taking the data outside of the semiconductor memory, in the case where a relationship between the two security levels identified by the level identification unit meets a predetermined condition.

2. **(Original)** A semiconductor memory according to Claim 1, further comprising a comparison unit operable to determine the relationship by comparing the two security levels identified by the level identification unit,

wherein the duplication unit is operable to duplicate the data stored in the first storage unit and to store the duplicated data into the second storage unit, in the case where the relationship determined by the comparison unit meets the predetermined condition.

3. **(Original)** A semiconductor memory according to Claim 1, further comprising an obtaining unit operable to obtain the relationship determined by comparing the two security levels identified by the level identification unit,

wherein the duplication unit is operable to duplicate the data stored in the first storage unit and to store the duplicated data into the second storage unit, in the case where the relationship obtained by the obtaining unit meets the predetermined condition.

4. (Original) A semiconductor memory according to Claim 1,

wherein the security level is a value corresponding to a strength of encryption used by each of the application programs.

5. (Original) A semiconductor memory according to Claim 4,

wherein the strength of the encryption is stronger as an algorithm of the encryption is more complex.

6. (Original) A semiconductor memory according to Claim 4,

wherein the strength of the encryption is stronger as a bit length of a key for the encryption is longer.

7. (Original) A semiconductor memory according to Claim 1,

wherein the security level is a value corresponding to a version number of an application protocol used by each of the application programs.

8. (Original) A semiconductor memory according to Claim 1,

wherein the security level is a value corresponding to a version number of each of the application programs.

9. (Currently Amended) A data duplication method used for a semiconductor memory which is capable of for executing at least two application programs,

wherein the semiconductor memory includes:

a first storage unit operable to store data relating to a first application program; and a second storage unit operable to store data relating to a second application program, and the data duplication method comprises:

identifying respective security levels of the first and second application programs for the data

relating to the first and second application programs, based on a criterion for identifying a security level of an application program for data relating to the application program; and

duplicating the data stored in the first storage unit and storing the duplicated data into the second storage unit, without taking the data outside of the semiconductor memory, in the case where a relationship between the identified two security levels meets a predetermined condition.

10. (Currently Amended) A program used for data duplication in a semiconductor memory which is capable of for executing at least two application programs,

wherein the semiconductor memory includes:

a first storage unit operable to store data relating to a first application program; and a second storage unit operable to store data relating to a second application program, and the program causes a computer to execute:

identifying respective security levels of the first and second application programs for the data relating to the first and second application programs, based on a criterion for identifying a security level of an application program for data relating to the application program; and

duplicating the data stored in the first storage unit and storing the duplicated data into the second storage unit, without taking the data outside of the semiconductor memory, in the case where a relationship between the identified two security levels meets a predetermined condition.

11. (Currently Amended) A computer-readable recording medium on which a program used for data duplication in a semiconductor memory is stored, the semiconductor memory being capable of <u>for</u> executing at least two application programs,

wherein the semiconductor memory includes:

a first storage unit operable to store data relating to a first application program; and a second storage unit operable to store data relating to a second application program, and the program causes a computer to execute:

identifying respective security levels of the first and second application programs for the data relating to the first and second application programs, based on a criterion for identifying a

security level of an application program for data relating to the application program; and duplicating the data stored in the first storage unit and storing the duplicated data into the second storage unit, without taking the data outside of the semiconductor memory, in the case where a relationship between the identified two security levels meets a predetermined condition.

12. **(Currently Amended)** An integrated circuit for controlling data duplication in a semiconductor memory which is capable of <u>for</u> executing at least two application programs, wherein the semiconductor memory includes:

a first storage unit operable to store data relating to a first application program; and a second storage unit operable to store data relating to a second application program, and the integrated circuit comprises:

a level identification unit operable to identify respective security levels of the first and second application programs for the data relating to the first and second application programs, based on a criterion for identifying a security level of an application program for data relating to the application program; and

a duplication unit operable to duplicate the data stored in the first storage unit and to store the duplicated data into the second storage unit, without taking the data outside of the semiconductor memory, in the case where a relationship between the two security levels identified by the level identification unit meets a predetermined condition.